



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/882,534
Confirmation No. 8714
Filing Date June 14, 2001
Inventor Garo J. Derderian, et al
Assignee Micron Technology, Inc.
Group Art Unit 2822
Examiner T. Thomas
Attorney's Docket No. MI22-1752
Customer No. 021567
Title: Capacitor Fabrication Methods and Capacitor Constructions

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

Commissioner for Patents
PO Box 1450
Art Unit 2822
Alexandria, VA 22313-1450

REMARKS

In compliance with 37 C.F.R. §§1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

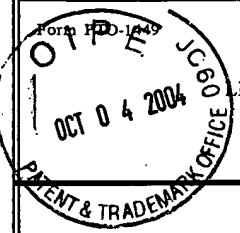
Date: 04 Oct 2001

By: James E. Lake
Reg. No. 44,854

10/12/2004 WABDELRI 00000035 09882534

02 FC:1806

180.00 0P

		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-1752		SERIAL NO. 09/882,534	
		LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)					
		APPLICANT Garo J. Derderian				FILING DATE June 14, 2001	
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,780,704	8/2004	Raaijmakers et al			
	AB	6,551,399	4/2003	Sneh et al			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AM						
	AN						
	AO						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
	AP						
	AQ						
	AR						
EXAMINER				DATE CONSIDERED			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							